

FIG. 1
PRIOR ART

FIG. 2
PRIOR ART

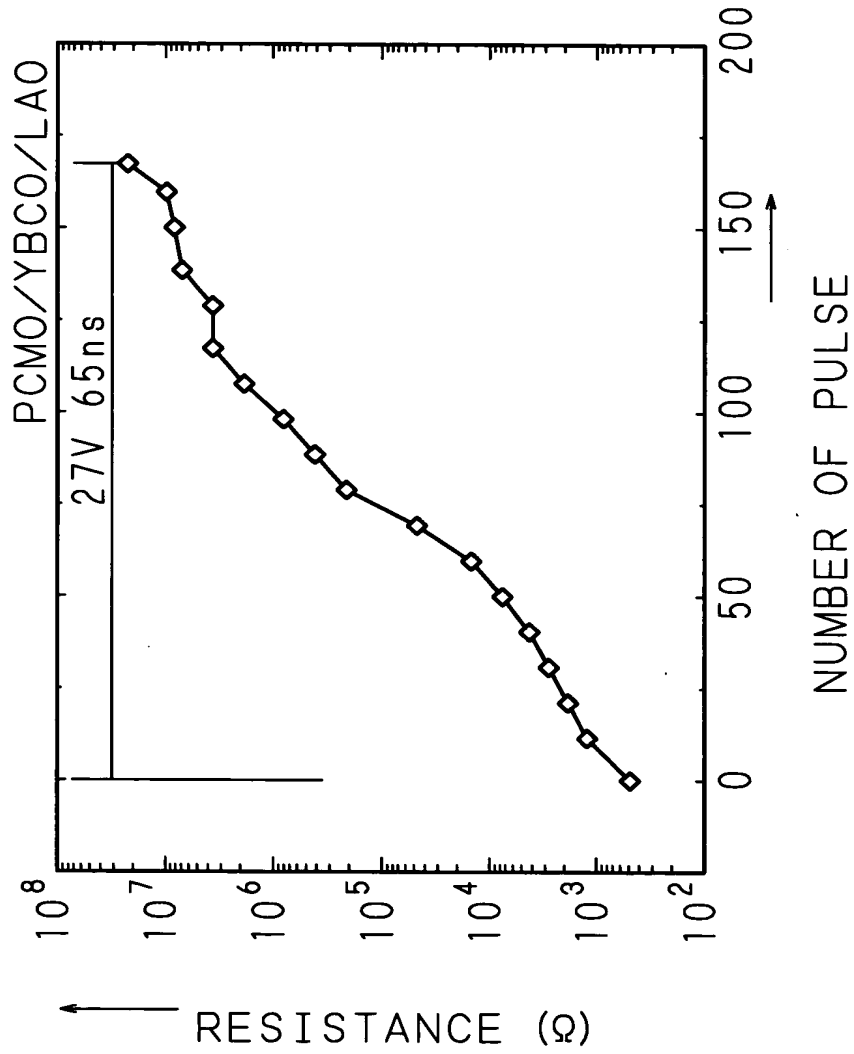


FIG. 3
 PRIOR ART

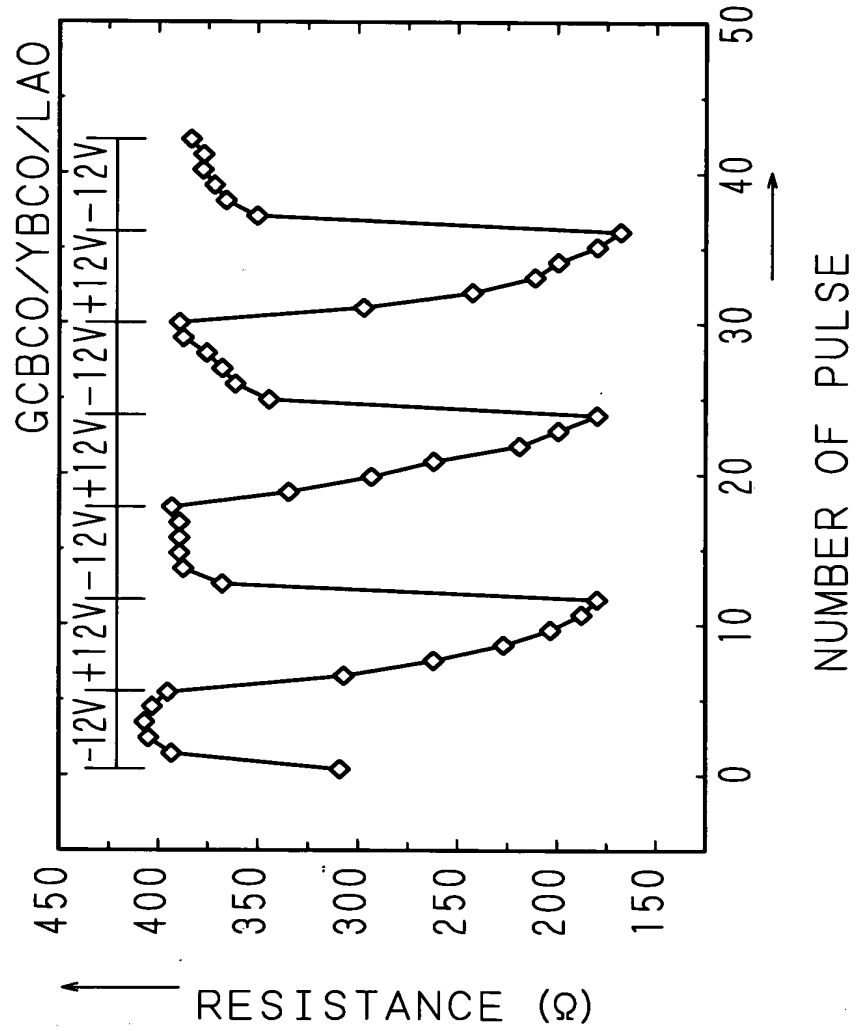


FIG. 4
 PRIOR ART

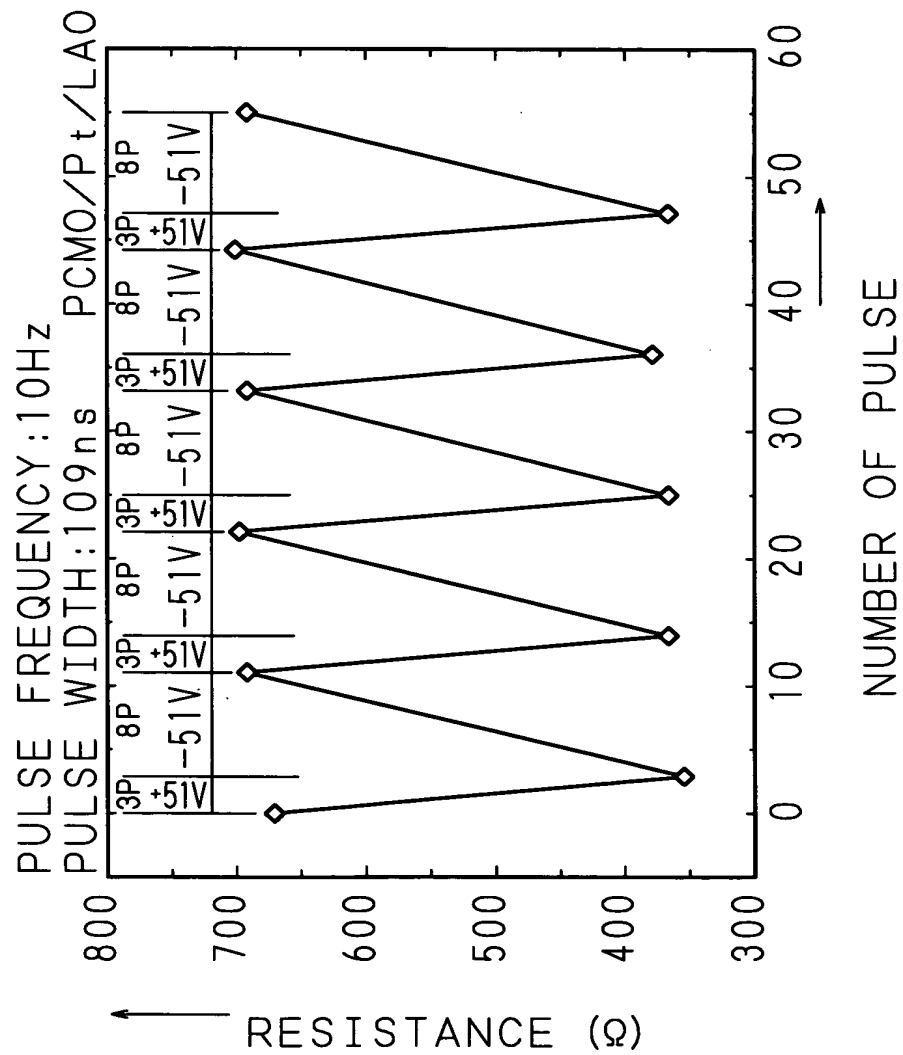


FIG. 5A

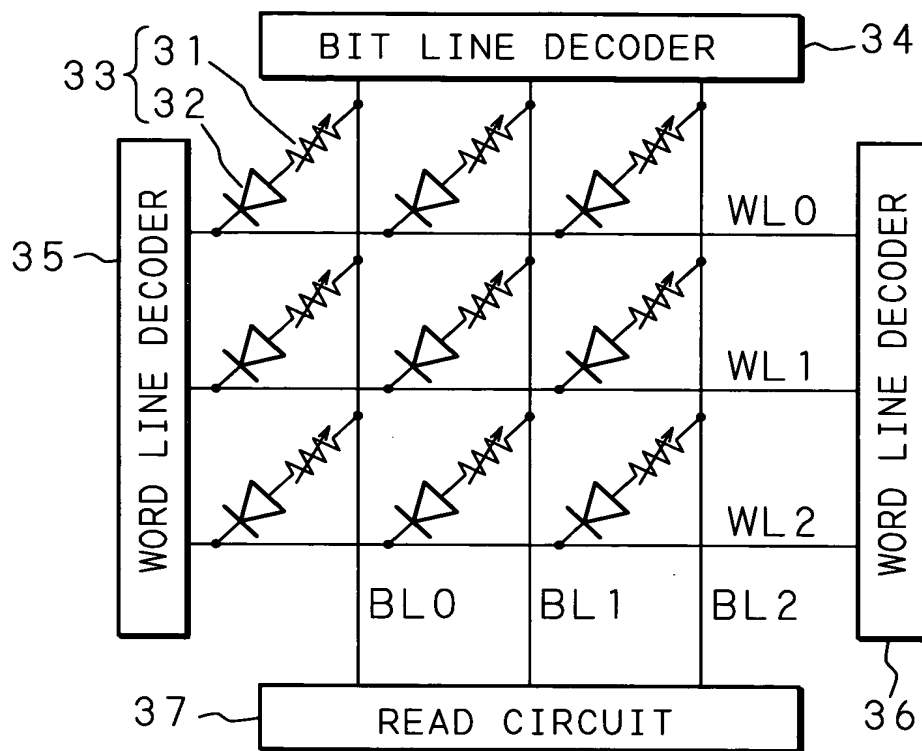
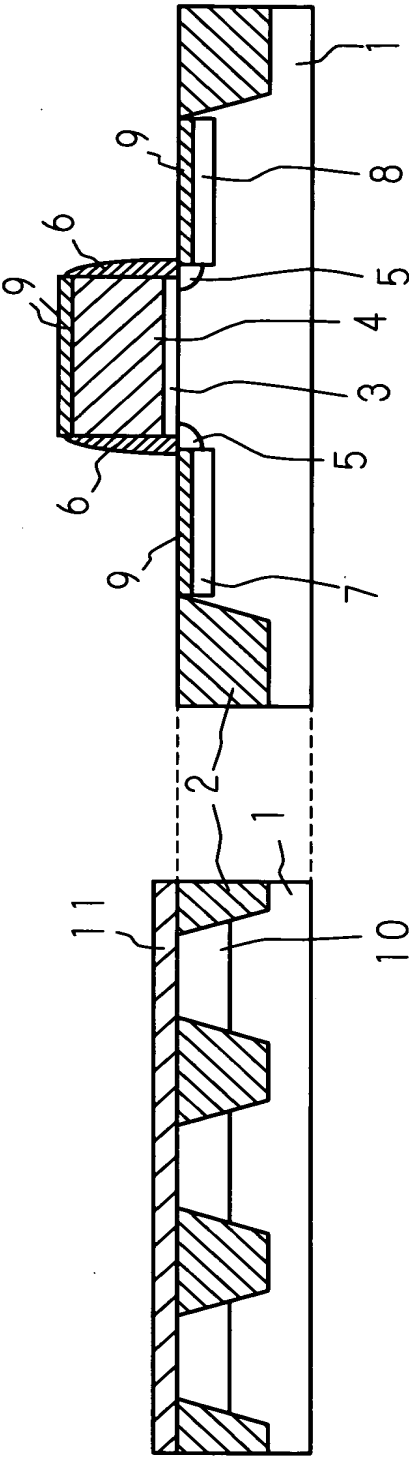


FIG. 5B

	VOLTAGE (BL)	VOLTAGE (WL)
SELECTED CELL	V_w	0
HALF-SELECTED CELL (BL SELECTION)	V_w	$V_w/2$
HALF-SELECTED CELL (WL SELECTION)	$V_w/2$	0
NON-SELECTED CELL	$V_w/2$	$V_w/2$

FIG. 6



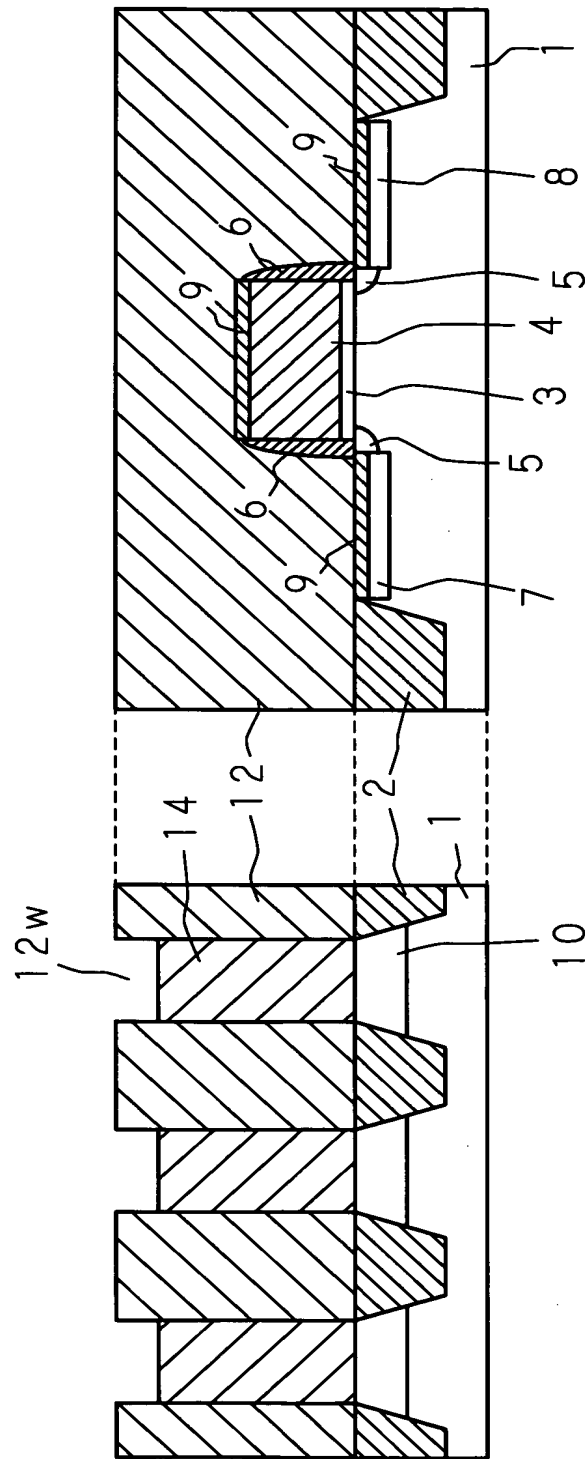
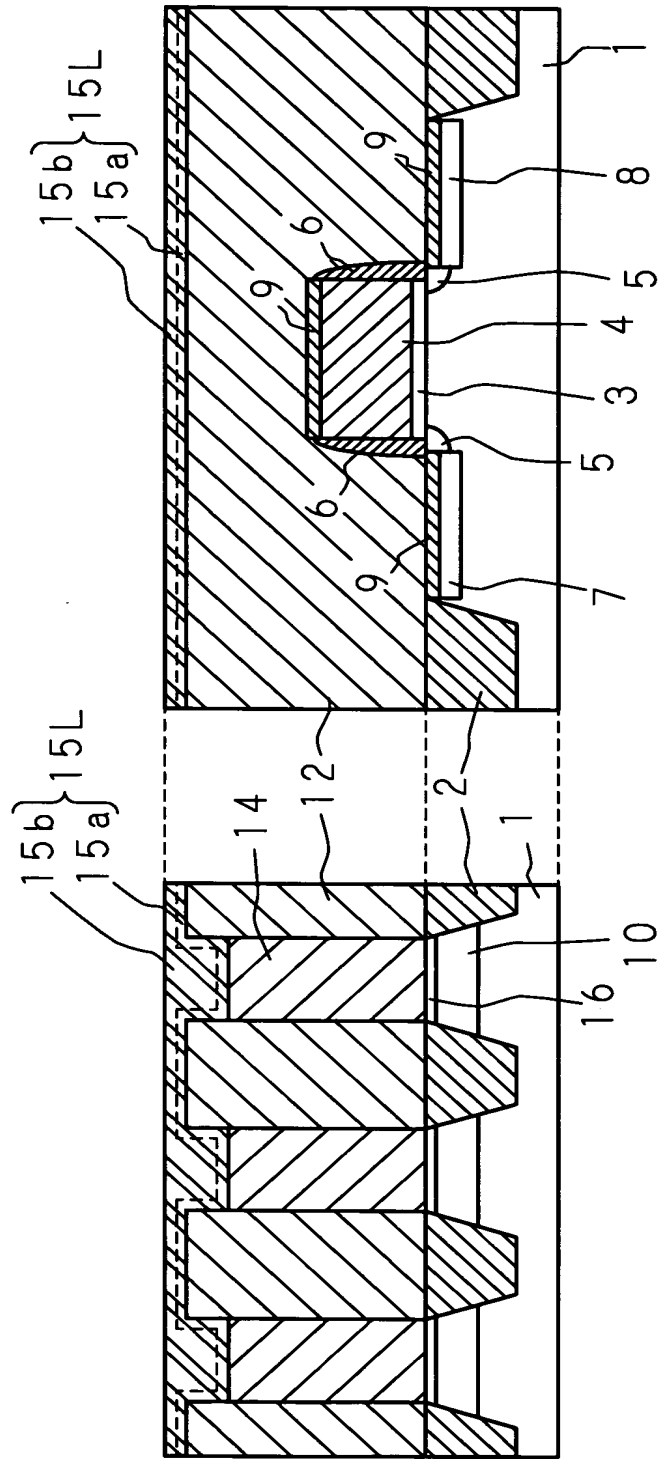
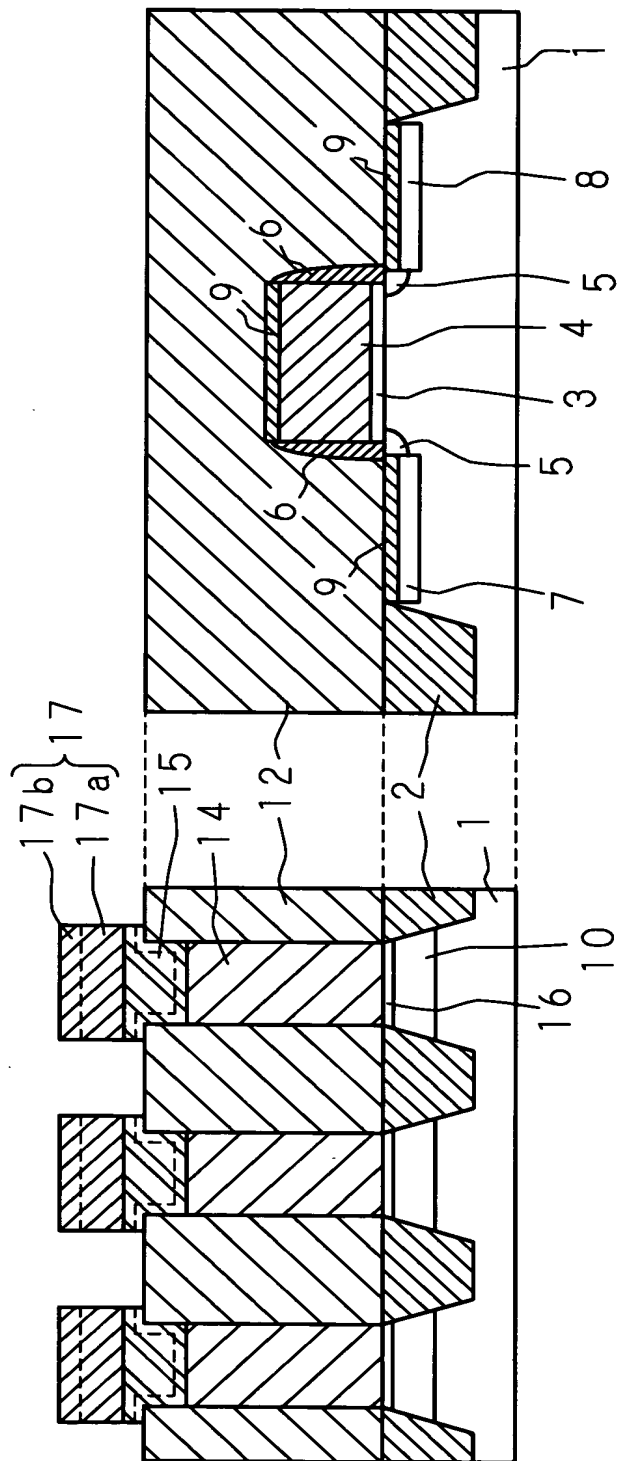


FIG. 8

FIG. 9





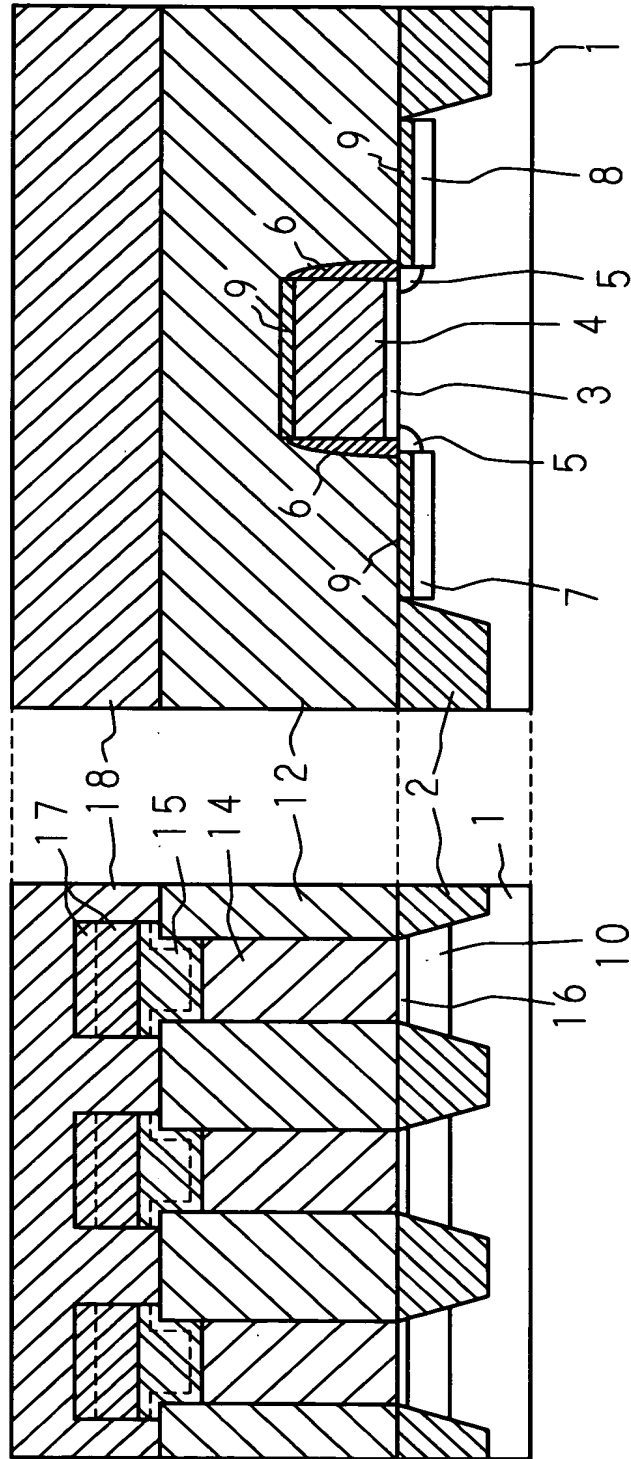


FIG. 11

FIG. 12

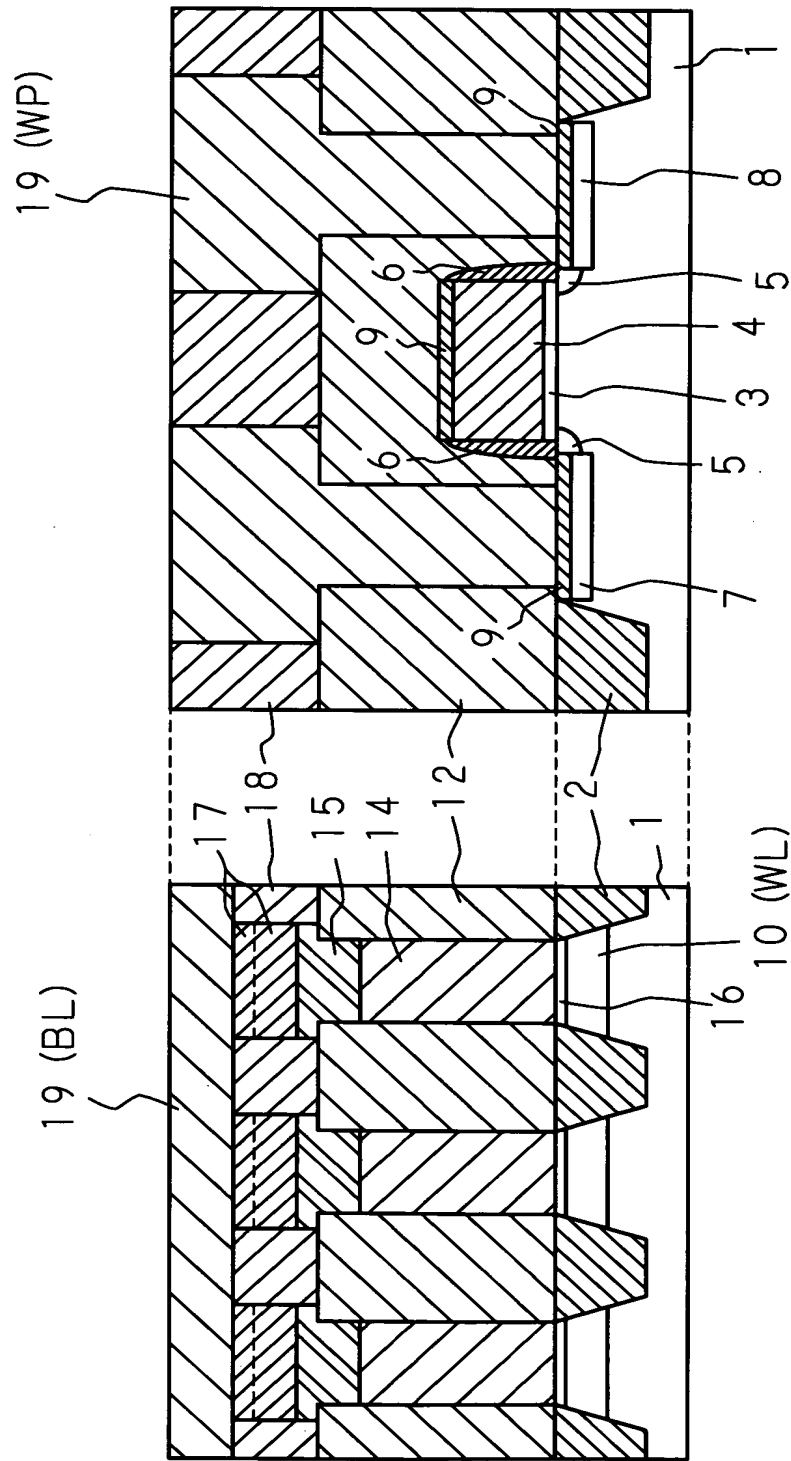
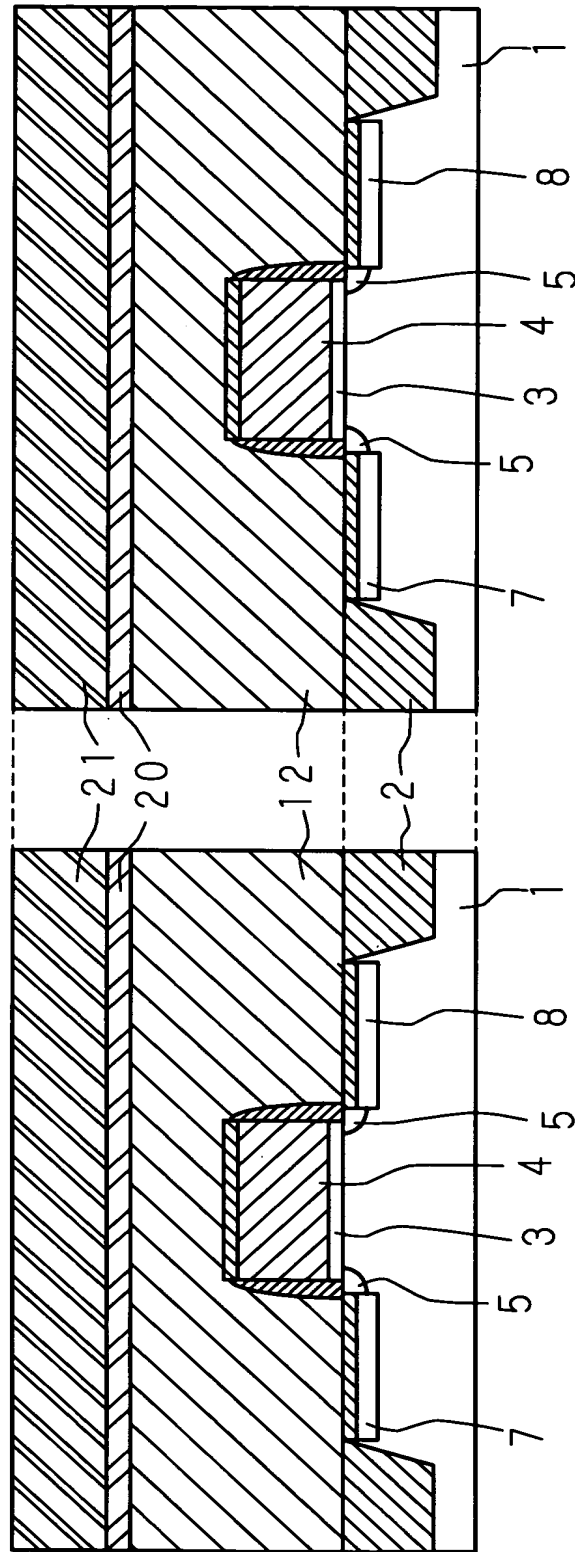


FIG. 13



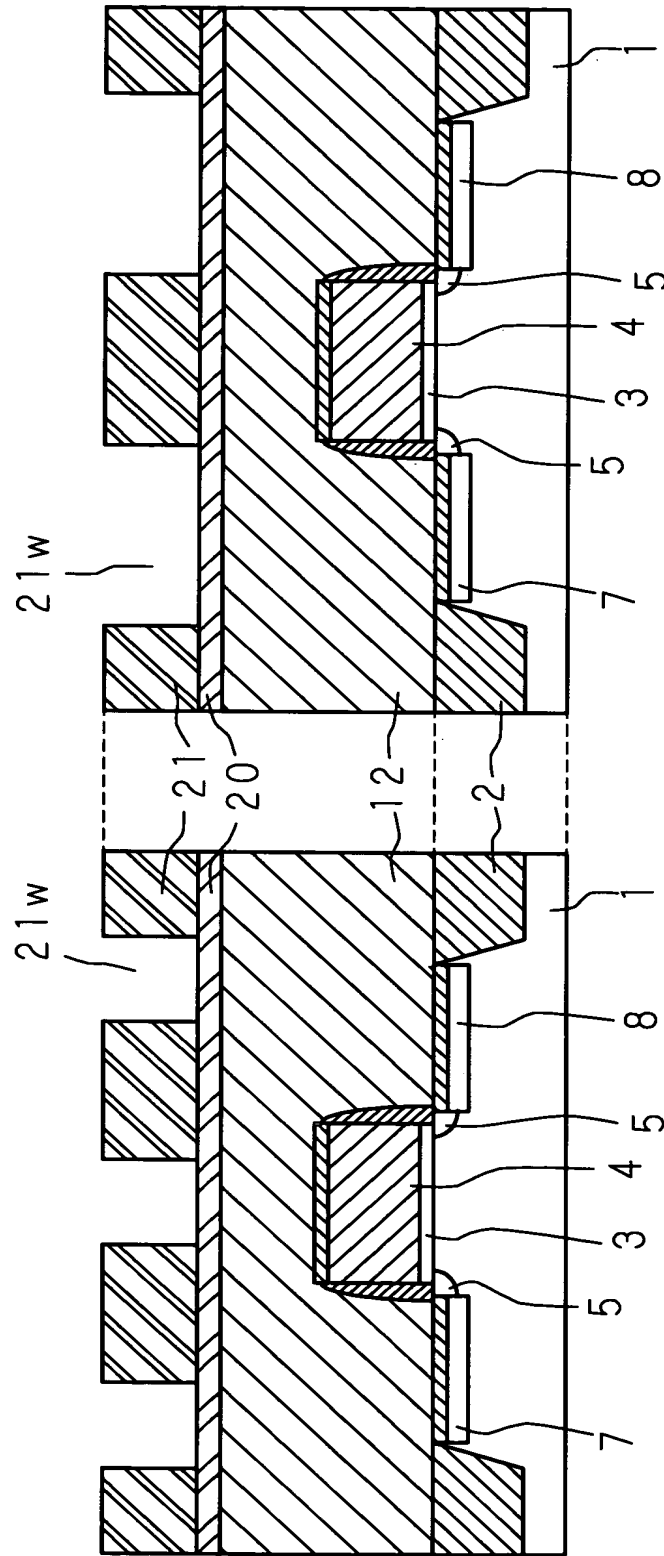


FIG. 14

FIG. 15

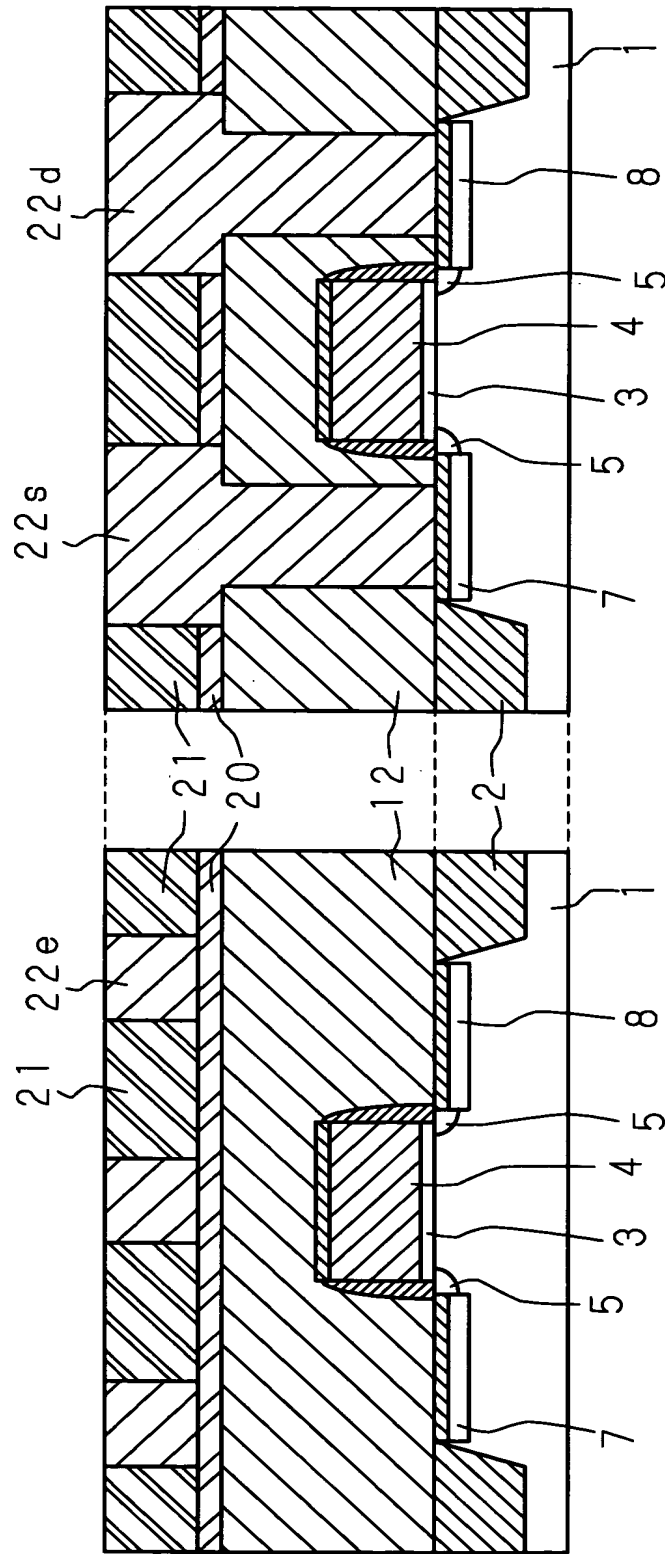


FIG. 17

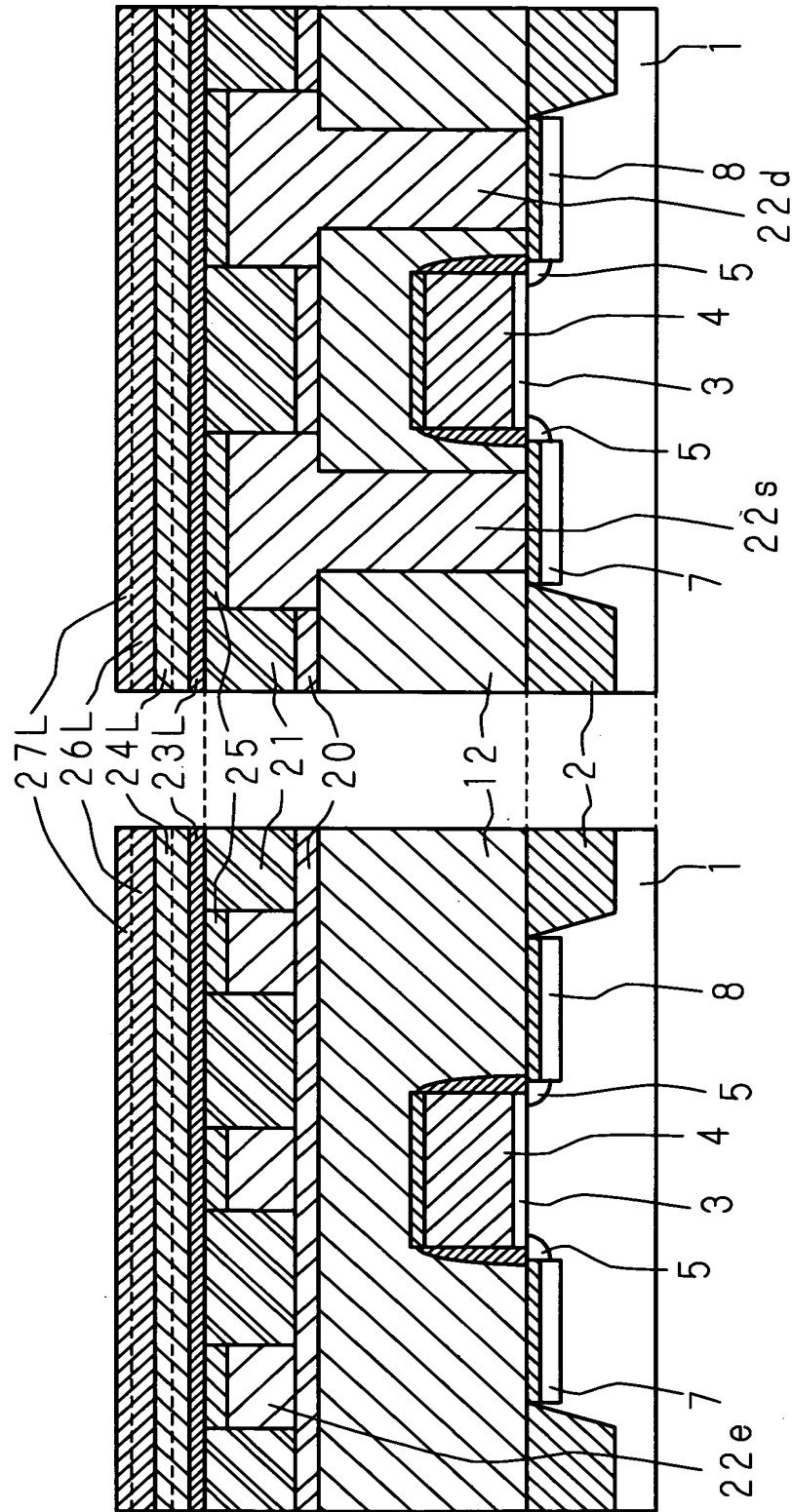


FIG. 18

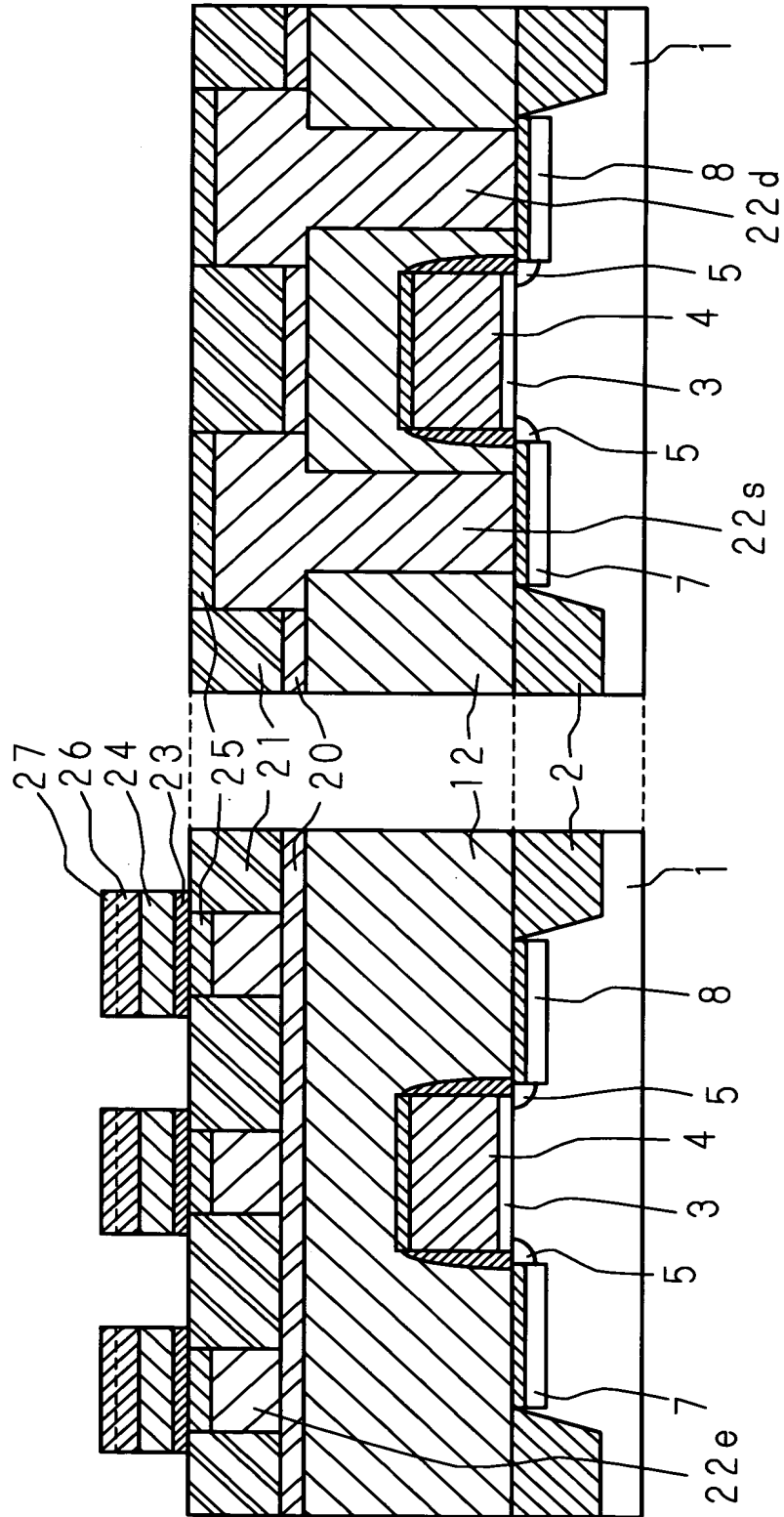


FIG. 19

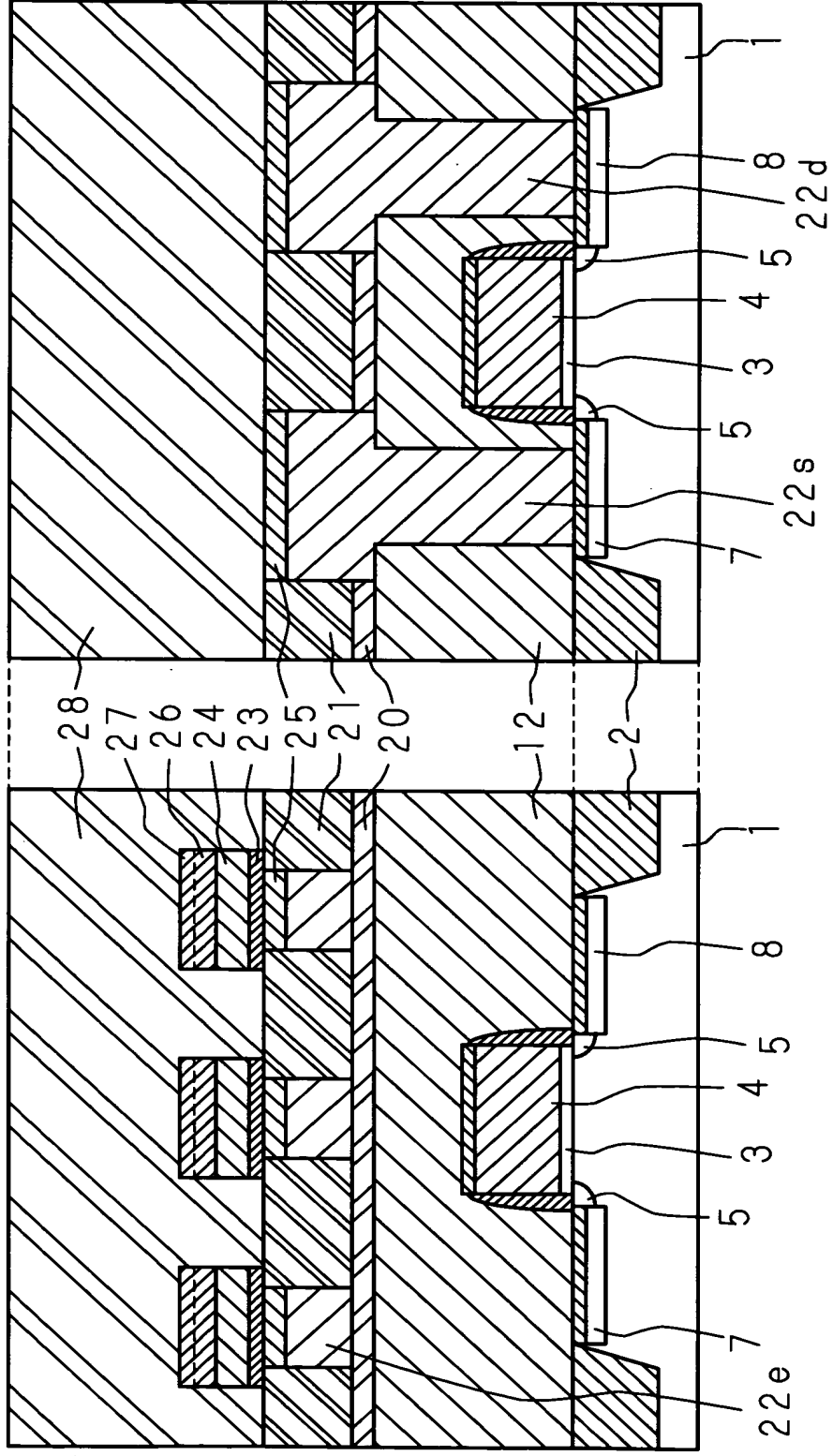


FIG. 20

